



PATENT  
Docket No. INTEL/17586

IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE

Applicants: Shi et al.	)	I hereby certify that this paper is
	)	being deposited with the United
Serial No.: 10/684,690	)	States Postal Service with
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	)	Commissioner for Patents, P.O.
Assignee: Intel Corporation	)	Box 1450, Alexandria, VA 22313-
	)	1450 on this date:
For: "Methods and Apparatus to Analyze	)	
Escape of Analysis of an Application"	)	November 12, 2003
	)	<u>Frankie Ho</u>
Group Art Unit: Not Yet Assigned	)	Frankie Ho
	)	Registration No. 48,479
Examiner: Not Yet Assigned	)	Attorney for Applicants

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

### TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

### METHOD OF PAYMENT

☒ No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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November 12, 2003

Attorneys for Intel Corporation

Form PTO-1449 (Modified)

U.S. Department of Commerce  
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Atty. Docket No.

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INTEL/17586

10/684,690

Applicant

Shi et al.

Filing Date

August 14, 2003

Group Art Unit

Not Yet Assigned

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01						

## FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes                      No	
	B01							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Choi et al., <i>Escape Analysis for Java</i> , IBM T.J. Watson Research Center, New York, pp. 1-19 (1999).					
	C02	Erik Ruf, <i>Effective Synchronization Removal for Java</i> , Microsoft Research, Washington, pp. 208-218 (2000).					

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.